

The RMC-TMG-1588 was developed as an external timing module add-on for the ATCA-PTSW-AMC4_RTM. Its main purpose is to generate the IRIG-B 100MHz clock and timecode signals for distribution to the ATCA-PTSW-AMC4 Front Board and, through the Front Boards’s FPGA (cross-point switch) CPS, to all system endpoints. CLK100MHz uses RTM TCLKA physical line and IRIG-B timecode uses TCLKD. The generated clock signals are synchronized with the IEEE-1588-2008 protocol, which core is implemented on the RMC-TMG-1588 local firmware. The module provides an RJ45 GbE connector where an external master clock unit should be connected. The IRIG-B clock and time code signals, as well as an auxiliary “pulse-per-second” (PPS) sync signal are made available at the external LEMO connectors.

