

Task Leader: Jorge Sousa

- The product ATCA-PTSW-AMC4 as defined in [ITER D 3ZH94V](#) will support CODAC TCN architecture and functions.
- Time and synchronization on the PCIESwitch card will use the absolute time in IRIG format and have a disciplined oscillator servo with 50 ns RMS of phase precision and accuracy (to be discussed during the kick-off meeting)
- The product will be xTCAcompatible – more specifically mTCA.4.

In early phase, 3 x ATCA-PTSW-AMC4 cards will be shipped to CODAC onsite

### **Sub-Tasks:**

#### **2.1. Production**

- 2.1.1. Manufacture
- 2.1.2. Board Test
- 2.1.3. Packaging and Delivery

#### **2.2. Software Support**

- 2.2.1. Linux Driver
- 2.2.2. EPICS Device Driver
- 2.2.3. Linux RPM Packaging
- 2.2.4. Test software
- 2.2.5. Firmware

#### **2.3. Documentation**

- 2.3.1. Engineering Design document
- 2.3.2. Installation and User Manuals
- 2.3.3. Test Plan

#### **2.4. Support**